

Model Reduction of Linear Switched Systems by Restricting Discrete Dynamics

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Abstract—We present a procedure for reducing the number of continuous states of discrete-time linear switched systems, such that the reduced system has the same behavior as the original system for a subset of switching sequences. The proposed method is expected to be useful for abstraction based control synthesis methods for hybrid systems.

I. INTRODUCTION

A discrete-time linear switched system [11], [19] (abbreviated by DTLSS) is a discrete-time hybrid system of the form

$$\Sigma \begin{cases} x(t+1) = A_{\sigma(t)}x(t) + B_{\sigma(t)}u(t) \text{ and } x(0) = x_0 \\ y(t) = C_{\sigma(t)}x(t), \end{cases} \quad (1)$$

where $x(t) \in \mathbb{R}^n$ is the continuous state, $y(t) \in \mathbb{R}^p$ the continuous output, $u(t) \in \mathbb{R}^m$ is the continuous input, $\sigma(t) \in Q = \{1, \dots, D\}$, $D > 0$ is the discrete state (switching signal). A_q, B_q, C_q are matrices of suitable dimension for $q \in Q$. A more rigorous definition of DTLSSs will be presented later on. For the purposes of this paper, $u(t)$ and $\sigma(t)$ will be viewed as externally generated signals.

Contribution of the paper Consider a discrete-time linear switched system Σ of the form (1), and a set L which describes the admissible set of switching sequences. In this paper, we will present an algorithm for computing another DTLSS

$$\bar{\Sigma} \begin{cases} \bar{x}(t+1) = \bar{A}_{\sigma(t)}\bar{x}(t) + \bar{B}_{\sigma(t)}u(t) \text{ and } \bar{x}(0) = \bar{x}_0 \\ \bar{y}(t) = \bar{C}_{\sigma(t)}\bar{x}(t) \end{cases} \quad (2)$$

such that for any switching sequence $\sigma(0) \dots \sigma(t) = q_0 \dots q_t \in L$ and continuous inputs $u(0), \dots, u(t-1)$, the output at time t of (1) equals the output of (2), i.e., $y(t) = \bar{y}(t)$ and the number of state variables of (2) is smaller than that of (1). In short, for any sequence of discrete states from L , the input-output behavior of Σ and $\bar{\Sigma}$ coincide and the size of $\bar{\Sigma}$ is smaller.

Motivation Realistic plant models of industrial interest tend to be quite large and in general, the smaller is the plant model, the smaller is the resulting controller and the computational complexity of the control synthesis or verification algorithm. This is especially apparent for hybrid systems, since in this case, the computational complexity of control or verification algorithms is often exponential in

the number of continuous states [20]. The particular model reduction problem formulated in this paper was motivated by the observation that in many instances, we are interested in the behavior of the model only for certain switching sequences. To illustrate this point, we will consider a number of simple scenarios where the results of the paper could potentially be useful.

(1) *Control and verification of DTLSSs with switching constraints.* DTLSSs with switching constraints occur naturally in a large number of applications. Such systems arise for example when the supervisory logic of the switching law is (partially) fixed. Note that verification or control synthesis of DTLSSs can be computationally demanding, especially if the properties or control objectives of interest are discrete [5]. The results of the paper could be useful for verification or control of such systems, if the properties of interest or the control objectives depend only on the input-output behavior. In this case, we could replace the original DTLSS Σ by the reduced order DTLSS $\bar{\Sigma}$ whose input-output behavior for all the admissible switching sequences coincides with that of Σ . We can then perform verification or control synthesis for $\bar{\Sigma}$ instead of Σ . If $\bar{\Sigma}$ satisfies the desired input-output properties, then so does Σ . Likewise, if the composition of $\bar{\Sigma}$ with a controller meets the control objectives, then the composition of this controller with Σ meets them too.

(2) *Piecewise-affine hybrid systems.* Consider a piecewise-linear hybrid system H [2], [21]. Such systems can often be modelled as a feedback interconnection of a linear switched system Σ of the form (1) with a discrete event generator ϕ , which generates the next discrete state based on the past discrete states and past outputs. As a consequence, the solutions of H corresponds to the solutions $\{q_t, x_t, u_t, y_t\}_{t=0}^{\infty}$ of (1) with $q_t = \phi(\{y_s, q_s\}_{s=0}^{t-1})$. A simple example of such a system is $q_t = \phi(y_{t-1})$, $t > 0$, and q_0 is fixed, where ϕ is a piecewise affine map. Often, it is desired to verify if the system is *safe*, i.e., that the sequences of discrete modes generated by the system H belong to a certain set of safe sequences L for all (some) continuous input signals. Consider now another piecewise-affine hybrid system \bar{H} obtained by interconnecting the discrete event generator ϕ with a reduced order DTLSS $\bar{\Sigma}$, such that the input-output behavior of $\bar{\Sigma}$ coincides with that of Σ for all the switching sequences from L . If L is prefix closed, then H is safe if and only if \bar{H} is safe, and hence it is sufficient to perform safety analysis on \bar{H} . Since the number of continuous-states of \bar{H} is smaller than that of H , it is easier to do verification for \bar{H} than for the original model. Note that verification of

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piecewise-affine hybrid systems has high (in certain cases exponential) computational complexity, [6], [22]. Likewise, assume that it is desired to design a control law for H which ensures that the switching signal generated by the closed-loop system belongs to a certain prefix closed set L . Such problems arise in various settings for hybrid systems [20]. Again, this problem is solvable for H if and only if it is solvable for \bar{H} , and the controller which solves this problem for \bar{H} also solves it for H .

Related work Results on realization theory of linear switched systems with constrained switching appeared in [15]. However, [15] does not yield a model reduction algorithm, see Remark 1 for a detailed discussion. The algorithm presented in this paper bears a close resemblance to the moment matching method of [1], but its result and its scope of application are different. The subject of model reduction for hybrid and switched systems was addressed in several papers [3], [25], [12], [4], [8], [23], [24], [7], [9], [10], [13], [18]. However, none of them deals with the problem addressed in this paper.

Outline In Section II, we fix the notation and terminology of the paper. In Section III, we present the formal definition and main properties of DTLSSs. In Section IV we give the precise problem statement. In Section V, we recall the concept of Markov parameters, and we present the fundamental theorem and corollaries which form the basis of the model reduction by moment matching procedure. The algorithm itself is stated in Section VI in detail. Finally, in Section VII the algorithm is illustrated on some numerical examples.

II. PRELIMINARIES: NOTATION AND TERMINOLOGY

Denote by \mathbb{N} the set of natural numbers including 0.

Consider a non-empty set Q which will be called the *alphabet*. Denote by Q^* the set of finite sequences of elements of Q . The elements of Q^* are called *strings* or *words* over Q , and any set $L \subseteq Q^*$ is called a *language* over Q . Each non-empty word w is of the form $w = q_1 q_2 \cdots q_k$ for some $q_1, q_2, \dots, q_k \in Q$, $k > 0$. In the following, if a word w is stated as $w = q_1 q_2 \cdots q_k$, it will be assumed that $q_1, q_2, \dots, q_k \in Q$. The element q_i is called the *i th letter* of w , for $i = 1, 2, \dots, k$ and k is called the *length* of w . The *empty sequence (word)* is denoted by ε . The length of word w is denoted by $|w|$; note that $|\varepsilon| = 0$. The set of non-empty words is denoted by Q^+ , i.e., $Q^+ = Q^* \setminus \{\varepsilon\}$. The set of words of length $k \in \mathbb{N}$ is denoted by Q^k . The *concatenation* of word $w \in Q^*$ with $v \in Q^*$ is denoted by wv : if $v = v_1 v_2 \cdots v_k$, and $w = w_1 w_2 \cdots w_m$, $k > 0, m > 0$, then $wv = v_1 v_2 \cdots v_k w_1 w_2 \cdots w_m$. If $v = \varepsilon$, then $wv = w$; if $w = \varepsilon$, then $wv = v$.

If Q has a finite number of elements, say D , it will be identified with its index set, that is $Q = \{1, 2, \dots, D\}$.

III. LINEAR SWITCHED SYSTEMS

In this section, we present the formal definition of linear switched systems and recall a number of relevant definitions. We follow the presentation of [15], [17].

Definition 1 (DTLSS): A discrete-time linear switched system (DTLSS) is a tuple

$$\Sigma = (p, m, n, Q, \{(A_q, B_q, C_q) | q \in Q\}, x_0) \quad (3)$$

where $Q = \{1, \dots, D\}$, $D > 0$, called the set of discrete modes, $A_q \in \mathbb{R}^{n \times n}$, $B_q \in \mathbb{R}^{n \times m}$, $C_q \in \mathbb{R}^{p \times n}$ are the matrices of the linear system in mode $q \in Q$, and x_0 is the initial state. The number n is called the *dimension (order)* of Σ and will sometimes be denoted by $\dim \Sigma$.

Notation 1: In the sequel, we use the following notation and terminology: The state space $X = \mathbb{R}^n$, the output space $Y = \mathbb{R}^p$, and the input space $U = \mathbb{R}^m$. We will write $\overline{U^+ \times Q^+} = \{(u, \sigma) \in U^+ \times Q^+ \mid |u| = |\sigma|\}$, and $\sigma(t)$ for the $t+1$ th element q_t of a sequence $\sigma = q_1 q_2 \cdots q_{|\sigma|} \in Q^+$ (the same comment applies to the elements of U^+ , X^+ and Y^+).

Throughout the paper, Σ denotes a DTLSS of the form (1).

Definition 2 (Solution): A *solution* of the DTLSS Σ at the initial state $x_0 \in X$ and relative to the pair $(u, \sigma) \in \overline{U^+ \times Q^+}$ is a pair $(x, y) \in X^+ \times Y^+$, $|x| = |\sigma| + 1$, $|y| = |\sigma|$ satisfying

$$\begin{aligned} x(t+1) &= A_{\sigma(t)} x(t) + B_{\sigma(t)} u(t), \quad x(0) = x_0 \\ y(t) &= C_{\sigma(t)} x(t), \end{aligned} \quad (4)$$

for $t = 0, 1, \dots, |\sigma| - 1$.

We shall call u the control input, σ the switching sequence, x the state trajectory, and y the output trajectory. Note that the pair $(u, \sigma) \in \overline{U^+ \times Q^+}$ can be considered as an input to the DTLSS.

Definition 3 (Input-state and input-output maps): The *input-state* map X_{Σ, x_0} and *input-output* map Y_{Σ, x_0} for the DTLSS Σ , induced by the initial state $x_0 \in X$, are the maps

$$\begin{aligned} \overline{U^+ \times Q^+} &\rightarrow X^+; (u, \sigma) \mapsto X_{\Sigma, x_0}(u, \sigma) = x, \\ \overline{U^+ \times Q^+} &\rightarrow Y^+; (u, \sigma) \mapsto Y_{\Sigma, x_0}(u, \sigma) = y, \end{aligned}$$

where (x, y) is the solution of Σ at x_0 relative to (u, σ) .

Next, we present the basic system theoretic concepts for DTLSSs. The input-output behavior of a DTLSS realization can be formalized as a map

$$f : \overline{U^+ \times Q^+} \rightarrow Y^+. \quad (5)$$

The value $f(u, \sigma)$ represents the output of the underlying (black-box) system. This system may or may not admit a description by a DTLSS. Next, we define when a DTLSS describes (realizes) a map of the form (5).

The DTLSS Σ of the form (1) is a *realization* of an input-output map f of the form (5), if f is the input-output map of Σ which corresponds to some initial state x_0 , i.e., $f = Y_{\Sigma, x_0}$. The map Y_{Σ, x_0} will be referred to as the *input-output map* of Σ , and it will be denoted by Y_Σ . The following discussion is valid only for realizable input-output maps.

We say that the DTLSSs Σ_1 and Σ_2 are *equivalent* if $Y_{\Sigma_1} = Y_{\Sigma_2}$. The DTLSS Σ_m is said to be a *minimal* realization of f , if Σ_m is a realization of f , and for any DTLSS Σ such that Σ is a realization of f , $\dim \Sigma_m \leq \dim \Sigma$. In [15], it is stated that a DTLSS realization Σ is minimal if and only if it is span-reachable and observable. See [15] for detailed definitions of span-reachability and observability for LSSs.

IV. MODEL REDUCTION BY RESTRICTING THE SET OF ADMISSIBLE SEQUENCES OF DISCRETE MODES

In this section, we state formally the problem of restricting the discrete dynamics of the DTLSS.

Definition 4: A non-deterministic finite state automaton (NFA) is a tuple $\mathcal{A} = (S, Q, \{\rightarrow_q\}_{q \in Q}, F, s_0)$ such that

- 1) S is the finite state set,
- 2) $F \subseteq S$ is the set of accepting (final) states,
- 3) $\rightarrow_q \subseteq S \times S$ is the state transition relation labelled by $q \in Q$,
- 4) $s_0 \in S$ is the initial state.

For every $v \in Q^*$, define \rightarrow_v inductively as follows: $\rightarrow_\varepsilon = \{(s, s) \mid s \in S\}$ and $\rightarrow_{vq} = \{(s_1, s_2) \in S \times S \mid \exists s_3 \in S : (s_1, s_3) \in \rightarrow_v \text{ and } (s_3, s_2) \in \rightarrow_q\}$ for all $q \in Q$. We denote the fact $(s_1, s_2) \in \rightarrow_v$ by $s_1 \rightarrow_v s_2$. The fact that there exists s_2 such that $s_1 \rightarrow_v s_2$ is denoted by $s_1 \rightarrow_v$. Define the language $L(\mathcal{A})$ accepted by \mathcal{A} as

$$L(\mathcal{A}) = \{v \in Q^* \mid \exists s \in F : s_0 \rightarrow_v s\}.$$

Recall that a language $L \subseteq Q^*$ is regular, if there exists an NFA \mathcal{A} such that $L = L(\mathcal{A})$. In this case, we say that \mathcal{A} *accepts* or *generates* L . We say that \mathcal{A} is *co-reachable*, if from any state a final state can be reached, i.e., for any $s \in S$, there exists $v \in Q^*$ and $s_f \in F$ such that $s \rightarrow_v s_f$. It is well-known that if \mathcal{A} accepts L , then we can always compute an NFA \mathcal{A}_{co-r} from \mathcal{A} such that \mathcal{A}_{co-r} accepts L and it is co-reachable. Hence, without loss of generality, in this paper we will consider only co-reachable NFAs.

Definition 5 (L-realization and L-equivalence): Consider an input-output map f and a DTLSS Σ . Let $L \subseteq Q^+$. We will say that Σ is an L -realization of f , if for every $u \in U^+$, and every $\sigma \in L$ such that $|u| = |\sigma|$,

$$Y_\Sigma(u, \sigma)(|\sigma| - 1) = f(u, \sigma)(|\sigma| - 1), \quad (6)$$

i.e., the final value of Y_Σ and f agrees for all $(u, \sigma) \in U^+ \times L$, $|\sigma| = |u|$. Note that a Q^+ -realization is precisely a realization. We will say that two DTLSS Σ_1 and Σ_2 are L -equivalent, if Σ_2 is an L -realization of Y_{Σ_1} (or equivalently if Σ_1 is an L -realization of Y_{Σ_2}).

Problem 1 (Model reduction preserving L-equivalence): Consider a minimal DTLSS Σ and let $L \subseteq Q^+$ be a regular language. Find a DTLSS $\tilde{\Sigma}$ such that $\dim \tilde{\Sigma} < \dim \Sigma$ and, Σ and $\tilde{\Sigma}$ are L -equivalent.

Remark 1: The problem of finding an L -realization of f was already addressed in [15], [14] for the continuous time case. There, it was shown that if Σ is a realization of f and M is a number which depends on the cardinality of the state-space of a deterministic finite state automaton accepting L , then it is possible to find a $\tilde{\Sigma}$ such that $\tilde{\Sigma}$ is an L -realization of f and

$$\dim \tilde{\Sigma} \leq M \dim \Sigma. \quad (7)$$

This result may also be extended for the discrete time case in a similar way. However, as (7) shows, the obtained L -realization can even be of higher dimension than the original system.

V. MODEL REDUCTION ALGORITHM: PRELIMINARIES

In order to present the model reduction algorithm and its proof of correctness, we need to recall the following definitions from [16].

Definition 6 (Convolution representation): The input-output map f has a *generalized convolution representation* (abbreviated as **GCR**), if there exist maps $S_0^f : Q^+ \rightarrow \mathbb{R}^p$, $S^f : Q^+ \rightarrow \mathbb{R}^{p \times m}$, such that $S^f(q) = 0$ if $q \in Q$ and

$$f(u, \sigma)(t) = S_0^f(q_0 q_1 \cdots q_t) + \sum_{k=0}^{t-1} S^f(q_k q_{k+1} \cdots q_t) u_k$$

for all $(u, \sigma) \in U^+ \times Q^+$, $t \leq |\sigma|$ with $\sigma = q_0 q_1 \cdots q_{|\sigma|}$. By a slight abuse of the terminology adopted in [16], we will call the maps $\{S^f, S_0^f\}$ the *Markov parameters* of f . Notice that if f has a **GCR**, then the Markov-parameters of f determine f uniquely. In other words, the Markov-parameters of f and g are equal if and only if f and g are the same input-output map, i.e. $S_0^f = S_0^g$ and $S^f = S^g$ if and only if $f = g$.

In the sequel, we will use the fact that Markov parameters can be expressed via the matrices of a state-space representation. In order to present this relationship, we introduce the following notation.

Notation 2: Let $w = q_1 q_2 \cdots q_k \in Q^*$, $k > 0$ and $A_{q_i} \in \mathbb{R}^{n \times n}$, $i = 1, \dots, k$. Then the matrix A_w is defined as

$$A_w = A_{q_k} A_{q_{k-1}} \cdots A_{q_1}. \quad (8)$$

If $w = \varepsilon$, then A_ε is the identity matrix.

Lemma 1 ([16]): The map f is realized by the DTLSS Σ if and only if f has a **GCR** and for all $v \in Q^*$, $q, q_0 \in Q$,

$$S^f(q_0 v q) = C_q A_v B_{q_0} \quad \text{and} \quad S_0^f(v q) = C_q A_v x_0. \quad (9)$$

We will extend Lemma 1 to characterize the fact that Σ is an L realization of f in terms of Markov parameters. To this end, we need the following notation.

Notation 3 (Prefix and suffix of L): Let the prefix $(L)_*$ and suffix $*(L)$ of a language L be defined as follows: $(L)_* = \{s \in Q^* \mid \exists w \in Q^* : sw \in L\}$, and $*(L) = \{s \in Q^* \mid \exists w \in Q^* : ws \in L\}$. In addition, let the 1-prefix $(L)_1$ and 1-suffix $_1(L)$ of a language L be defined as follows: $(L)_1 = \{s \in Q^* \mid \exists q \in Q : sq \in L\}$ and $_1(L) = \{s \in Q^* \mid \exists q \in Q : qs \in L\}$. A language L is said to be prefix (resp. suffix) closed if $(L)_* = L$ (resp. $*(L) = L$).

Example 1: Let the language L be defined as $L = (123)^* 12 = \{12, 12312, 12312312, \dots\}$. Then with the notation stated above, the following languages can be defined as follows:

$$\begin{aligned} *(L) &= \{\varepsilon, 2, 12, 312, 2312, 12312, \dots\} \\ (*(L))_1 &= \{\varepsilon, 1, 31, 231, 1231, \dots\} \\ ((*(L))_1)_* &= \{\varepsilon, 1, 2, 3, 12, 23, 31, 123, 231, 312, \dots\} \\ (L)_1 &= \{1, 1231, 1231231, \dots\} \\ ((L)_1)_* &= \{\varepsilon, 1, 12, 123, 1231, 12312, \dots\}. \end{aligned}$$

Note that if L is regular, then so are $(L)_*$, $*(L)$, $(L)_1$, $_1(L)$. Moreover NFAs accepting these languages can easily be computed from an NFA which accepts L .

The proof of Lemma 1 can be extended to prove the following result, which will be central for our further analysis.

Lemma 2: Assume f has a GCR. The DTLSS Σ is an L -realization of f , if and only if for all $v \in Q^*$, $q_0, q \in Q$

$$\begin{aligned} vq \in L &\implies S_0^f(vq) = C_q A_v x_0 \\ q_0 vq \in {}_*(L) &\implies S^f(q_0 vq) = C_q A_v B_{q_0}. \end{aligned} \quad (10)$$

Lemma 1 implies the following important corollary.

Corollary 1: Σ is L -equivalent to $\bar{\Sigma} = (p, m, r, Q, \{(\bar{A}_q, \bar{B}_q, \bar{C}_q) | q \in Q\}, \bar{x}_0)$ if and only if

- (i) $\forall q \in Q, v \in Q^* : vq \in L \implies C_q A_v x_0 = \bar{C}_q \bar{A}_v \bar{x}_0.$
- (ii) $\forall q, q_0 \in Q, v \in Q^* : q_0 vq \in {}_*(L) \implies C_q A_v B_{q_0} = \bar{C}_q \bar{A}_v \bar{B}_{q_0}.$

That is, in order to find a DTLSS $\bar{\Sigma}$ which is an L -equivalent realization of Σ , it is sufficient to find a DTLSS $\bar{\Sigma}$ which satisfies parts (i) and (ii) of Corollary 1. Intuitively, the conditions (i) and (ii) mean that certain Markov parameters of the input-output maps of $\bar{\Sigma}$ match the corresponding Markov parameters of the input-output map of Σ . Note that L need not be finite, and hence we might have to match an infinite number of Markov parameters. Relying on the intuition of [1], the matching of the Markov parameters can be achieved either by restricting Σ to the set of all states which are reachable along switching sequences from L , or by eliminating those states which are not observable for switching sequences from L . Remarkably, these two approaches are each other's dual.

Below we will formalize this intuition. This this end we use the following notation

Notation 4: In the sequel, the image and kernel of a real matrix M are denoted by $\text{im}(M)$ and $\text{ker}(M)$ respectively. In addition, the $n \times n$ identity matrix is denoted by I_n .

Definition 7 (L -reachability space): For a DTLSS Σ and $L \subseteq Q^*$, define the L -reachability space $\mathcal{R}_L(\Sigma)$ as follows:

$$\begin{aligned} \mathcal{R}_L(\Sigma) = \text{span} \Big\{ \{A_v x_0 \mid v \in Q^*, v \in ((L)_1)_*\} \cup \\ \{\text{im}(A_v B_{q_0}) \mid v \in Q^*, q_0 \in Q, q_0 v \in {}_*(L)_1\} \Big\}. \end{aligned} \quad (11)$$

Whenever Σ is clear from the context, we will denote $\mathcal{R}_L(\Sigma)$ by \mathcal{R}_L .

Recall that according to Notation 3

$$\begin{aligned} ((L)_1)_* &= \{s \in Q^* \mid \exists v \in Q^*, \hat{q} \in Q : sv\hat{q} \in L\}, \\ ({}_*(L)_1)_* &= \{s \in Q^* \mid \exists v_1, v_2 \in Q^*, \hat{q} \in Q : v_1 s v_2 \hat{q} \in L\}. \end{aligned} \quad (12)$$

Intuitively, the L -reachability space $\mathcal{R}_L(\Sigma)$ of a DTLSS Σ is the space consisting of all the states $x \in X$ which are reachable from x_0 with some continuous input and some switching sequence from L . It follows from [15], [19] that Σ is span-reachable if and only if $\dim \mathcal{R}_{Q^*} = n$.

Definition 8 (L -unobservability subspace): For a DTLSS Σ , and $L \subseteq Q^*$, define the L -unobservability subspace as

$$\mathcal{O}_L(\Sigma) = \bigcap_{v \in Q^*, q \in Q, vq \in {}_*(L)} \text{ker}(C_q A_v). \quad (13)$$

If Σ is clear from the context, we will denote $\mathcal{O}_L(\Sigma)$ by \mathcal{O}_L . Recall that according to Notation 3,

$${}_*(L) = \{s \in Q^* \mid \exists v \in Q^* : vs \in L\}. \quad (14)$$

Intuitively, the L -unobservability space $\mathcal{O}_L(\Sigma)$ is the set of all those states which remain unobservable under switching sequences from L .

From [19], it follows that Σ is observable if and only if $\mathcal{O}_{Q^*} = \{0\}$. Note that L -unobservability space is not defined in a totally “symmetric” way to the L -reachability space, i.e., subscript of the intersection sign in Equation (13) is not $vq \in {}_*(L)$. See Remarks 2 and 3 for further discussion.

We are now ready to present two results which are central to the model reduction algorithm to be presented in the next section.

Lemma 3: Let $\Sigma = (p, m, n, Q, \{(A_q, B_q, C_q) | q \in Q\}, x_0)$ be a DTLSS and $L \subseteq Q^*$. Let $\dim \mathcal{R}_L(\Sigma) = r$ and $P \in \mathbb{R}^{n \times r}$ be a full column rank matrix such that

$$\mathcal{R}_L(\Sigma) = \text{im}(P).$$

Let $\bar{\Sigma} = (p, m, r, Q, \{(\bar{A}_q, \bar{B}_q, \bar{C}_q) | q \in Q\}, \bar{x}_0)$ be the DTLSS defined by

$$\bar{A}_q = P^{-1} A_q P, \bar{B}_q = P^{-1} B_q, \bar{C}_q = C_q P, \bar{x}_0 = P^{-1} x_0,$$

where P^{-1} is a left inverse of P . Then $\bar{\Sigma}$ and Σ are L -equivalent.

That is, Lemma 3 says that if we find a matrix representation of the L -reachability space, then we can compute a reduced order DTLSS which is an L -realization of Σ .

Before presenting the proof of Lemma 3, we will prove the following claim.

Claim 1: With the conditions of Lemma 3 the following holds:

- (i) For all $v \in Q^*$ such that $v \in ((L)_1)_*$,

$$\begin{aligned} v = \varepsilon &\implies PP^{-1} x_0 = x_0, \\ v = q_1 \cdots q_k, k \geq 1 &\implies \\ PP^{-1} A_{q_k} \cdots PP^{-1} A_{q_1} PP^{-1} x_0 &= A_{q_k} \cdots A_{q_1} x_0. \end{aligned}$$

- (ii) For all $v \in Q^*$, $q_0 \in Q$ such that $q_0 v \in ({}_*(L)_1)_*$,

$$\begin{aligned} v = \varepsilon &\implies PP^{-1} B_{q_0} = B_{q_0}, \\ v = q_1 \cdots q_k, k \geq 1 &\implies \\ PP^{-1} A_{q_k} \cdots PP^{-1} A_{q_1} PP^{-1} B_{q_0} &= A_{q_k} \cdots A_{q_1} B_{q_0}. \end{aligned}$$

Proof: (Claim 1 (ii)) The proof is by induction on the length of v . For $|v| = 0$, let $q_0 \in Q$ and $q_0 v \in ({}_*(L)_1)_*$. The assumption $\mathcal{R}_L(\Sigma) = \text{im}(P)$ in Lemma 3 implies $\text{im}(B_{q_0}) \subseteq \text{im}(P)$. Hence, there exists an $\Lambda \in \mathbb{R}^{r \times m}$ such that $P\Lambda = B_{q_0}$, and therefore $PP^{-1} B_{q_0} = PP^{-1} P\Lambda = P\Lambda = B_{q_0}$.

For $|v| = k \geq 1$, $k \in \mathbb{N}$, let $v = q_1 \cdots q_k$, $q_0 v \in ({}_*(L)_1)_*$. Observe that if $q_0 v \in ({}_*(L)_1)_*$ then also $q_0 \hat{v} \in ({}_*(L)_1)_*$ where $\hat{v} = q_1 \cdots q_{k-1}$, since the set $({}_*(L)_1)_*$ is prefix closed. Assume the claim holds for $|v| = k - 1$, i.e., for $v = q_1 \cdots q_{k-1}$. Then

$$\begin{aligned} PP^{-1} A_{q_k} PP^{-1} A_{q_{k-1}} \cdots PP^{-1} A_{q_1} PP^{-1} B_{q_0} &= \\ PP^{-1} A_{q_k} A_{q_{k-1}} \cdots A_{q_1} B_{q_0}. \end{aligned}$$

Since again $\text{im}(A_{q_k} \cdots A_{q_1} B_{q_0}) \subseteq \text{im}(P)$, it follows that

$$PP^{-1}A_{q_k}A_{q_{k-1}} \cdots A_{q_1}B_{q_0} = A_{q_k}A_{q_{k-1}} \cdots A_{q_1}B_{q_0},$$

proving this part. The proof of part (i) is similar. ■

Proof: [Proof of Lemma 3] We will show that part (i) and (ii) of Corollary 1 hold.

(ii). Using Claim 1 (ii) and observing $(*(L))_1 \subseteq ((*(L))_1)_*$, it follows that, for all $v \in Q^*$, $q_0, q \in Q$ such that $q_0 v q \in *(L)$

$$v \in \varepsilon \implies \bar{C}_q \bar{B}_{q_0} = C_q PP^{-1} B_{q_0} = C_q B_{q_0},$$

$$v = q_1 \cdots q_k, k \geq 1 \implies$$

$$\bar{C}_q \bar{A}_v \bar{B}_{q_0} = C_q PP^{-1} A_{q_k} PP^{-1} A_{q_{k-1}} \cdots PP^{-1} A_{q_1} PP^{-1} B_{q_0} =$$

$$C_q A_{q_k} A_{q_{k-1}} \cdots A_{q_1} B_{q_0}.$$

(i) Similar to part (ii). ■

By similar arguments we also obtain:

Lemma 4: Let $\Sigma = (p, m, n, Q, \{(A_q, B_q, C_q) | q \in Q\}, x_0)$ be a DTLSS and let $L \subseteq Q^*$. Let $\text{codim } \mathcal{O}_L(\Sigma) = r$ and $W \in \mathbb{R}^{r \times n}$ be a full row rank matrix such that

$$\mathcal{O}_L(\Sigma) = \ker(W).$$

Let $\bar{\Sigma} = (p, m, r, Q, \{(\bar{A}_q, \bar{B}_q, \bar{C}_q) | q \in Q\}, \bar{x}_0)$ be the DTLSS defined by

$$\bar{A}_q = W A_q W^{-1}, \bar{B}_q = W B_q, \bar{C}_q = C_q W^{-1}, \bar{x}_0 = W x_0.$$

where W^{-1} is a right inverse of W . Then $\bar{\Sigma}$ is an L -equivalent to Σ .

Remark 2: (Lemma 3) Observe that from Lemma 2, the only Markov parameters involved in the output at a final state of the NDFA are of the form $C_q A_v x_0$ where $v \in (L)_1$ and $C_q A_v B_{q_0}$ where $q_0 v \in (*(L))_1$. However, for the induction in the proof of Claim 1 to work out, it is crucial that the words $v, q_0 v$ which are indexing the elements of the space $\text{span}\{A_v x_0, \text{im}(A_v B_{q_0})\}$, must belong to prefix closed sets. Since the smallest prefix-closure of a language K must be $(K)_*$, the prefix-closure of the sets $(L)_1$ and $(*(L))_1$ are used in the definition of L -reachability space; namely the sets $((L)_1)_*$ and $((*(L))_1)_*$ respectively. This fact leads to matching all the outputs (or the Markov parameters involved in the output) of the original and reduced order systems generated in the course of a switching sequence from L , as opposed to matching only the final outputs. The latter would be sufficient for L -equivalence.

Remark 3: (Lemma 4) Observe that from Lemma 2, the only Markov parameters involved in the output at a final state of the NDFA are of the form $C_q A_v x_0$ and $C_q A_v B_{q_0}$ where $v q \in *(L)$. In addition, for the induction in the proof of the counterpart of Claim 1 to function (in the case of Lemma 4), it suffices that the words $v q$ which are indexing the elements of the space $\cap \ker(C_q A_v)$ belong to a suffix-closed set. Since $*(L)$ is already suffix-closed, in this case there is no need to expand the set $*(L)$ for the definition of L -unobservability space. Hence the reduced order system found by the use of Lemma 4 will be an L -realization, but it need not be anything more. These will be illustrated in the last section with numerical examples.

VI. MODEL REDUCTION ALGORITHM

In this section, we present an algorithm for solving Problem 1. The proposed algorithm relies on computing the matrices P and W which satisfy the conditions of Lemma 3 and Lemma 4 respectively. In order to compute these matrices, we will formulate alternative definitions of L -reachability/unobservability spaces. To this end, for matrices G, H of suitable dimensions and for a regular language $K \subseteq Q^*$ define the sets $\mathcal{R}_K(G)$ and $\mathcal{O}_K(H)$ as follows;

$$\mathcal{R}_K(G) = \text{span}\{\text{im}(A_v G) \mid v \in K\}$$

$$\mathcal{O}_K(H) = \bigcap_{v \in K} \ker(H A_v).$$

Then the L -reachability space of Σ can be written as

$$\mathcal{R}_L = \mathcal{R}_{((L)_1)_*}(x_0) + \sum_{q \in Q} \mathcal{R}_{(q(K))}(B_q), \quad (15)$$

where $((L)_1)_*$ is defined as in (12), and

$${}^q(K) = \{s \in Q^* \mid \exists v_1, v_2 \in Q^*, \hat{q} \in Q, v_1 q s v_2 \hat{q} \in L\}. \quad (16)$$

In (15), $+$ and \sum denote sums of subspaces, i.e. if \mathcal{W}, \mathcal{V} are two linear subspaces of \mathbb{R}^n , then $\mathcal{W} + \mathcal{V} = \{a + b \mid a \in \mathcal{W}, b \in \mathcal{V}\}$. Similarly, if $\{\mathcal{W}_i\}_{i \in I}$ is a finite family of linear subspaces of \mathbb{R}^n , then $\sum_{i \in I} \mathcal{W}_i = \{\sum_{i \in I} a_i \mid a_i \in \mathcal{W}_i, i \in I\}$.

The L -unobservability space can be written as

$$\mathcal{O}_L = \bigcap_{q \in Q} \mathcal{O}_{((K)^q)}(C_q), \quad (17)$$

where

$$(K)^q = \{s \in Q^* \mid \exists v \in Q^*, v s q \in L\}. \quad (18)$$

Note that if L is regular, then ${}^q(K)$ and $(K)^q$, $q \in Q$, and $((L)_1)_*$ are also regular and NDFA's accepting ${}^q(K)$, $(K)^q$, $q \in Q$, and $((L)_1)_*$ can easily be computed from an NDFA accepting L . From (15) and (17) it follows that in order to compute the matrix P in Lemma 3 or W in Lemma 4, it is enough to compute representations of the subspaces $\mathcal{R}_K(G)$ and $\mathcal{O}_K(H)$ for various choices of K, G and H . The corresponding algorithms are presented in Algorithm 1 and Algorithm 2. There, we used the following notation.

Notation 5 (orth): For a matrix M , $\text{orth}(M)$ denotes the matrix U whose columns form an orthogonal basis of $\text{im}(M)$.

Lemma 5 (Correctness of Algorithm 1 – Algorithm 2): Algorithm 1 computes $\mathcal{R}_K(G)$ and Algorithm 2 computes $\mathcal{O}_K(H)$.

Proof: We prove only the first statement of the lemma, the second one can be shown using duality. Let $P_{s,i} = \text{span}\{\text{im}(A_v G) \mid v \in Q^*, |v| \leq i, s_0 \rightarrow_v s\}$. It then follows that after the execution of Step 2, $\text{im}(P_s) = P_{s,0}$ for all $s \in S$. Moreover, by induction it follows that

$$P_{s,i+1} = P_{s,i} + \sum_{q \in Q, s' \in S, s' \rightarrow_q s} A_q P_{s',i}$$

for all $i = 0, 1, \dots$ and $s \in S$. Hence, by induction it follows that at the i th iteration of the loop in Step 4, $\text{im}(P_s) = P_{s,i}$.

Algorithm 1 Calculate a matrix representation of $\mathcal{R}_K(G)$,
Inputs: $(\{A_q\}_{q \in Q}, G)$ and $\mathcal{A} = (S, \{\rightarrow_q\}_{q \in Q}, F, s_0)$ such that $L(\mathcal{A}) = K$, $F = \{s_{f_1}, \dots, s_{f_k}\}$, $k \geq 1$ and \mathcal{A} is co-reachable.
Outputs: $\hat{P} \in \mathbb{R}^{n \times \hat{r}}$ such that $\hat{P}^T \hat{P} = I_{\hat{r}}$, $\text{rank}(\hat{P}) = \hat{r}$, $\text{im}(\hat{P}) = \mathcal{R}_K(G)$.

```

1:  $\forall s \in S \setminus \{s_0\} : P_s := 0.$ 
2:  $P_{s_0} := \text{orth}(G).$ 
3:  $\text{flag} = 0.$ 
4: while  $\text{flag} = 0$  do
5:    $\forall s \in S : P_s^{\text{old}} := P_s$ 
6:   for  $s \in S$  do
7:      $W_s := P_s$ 
8:     for  $q \in Q, s' \in S : s' \rightarrow_q s$  do
9:        $W_s := \begin{bmatrix} W_s & A_q P_{s'}^{\text{old}} \end{bmatrix}$ 
10:    end for
11:     $P_s := \text{orth}(W_s)$ 
12:  end for
13:  if  $\forall s \in S : \text{rank}(P_s) = \text{rank}(P_s^{\text{old}})$  then
14:     $\text{flag} = 1.$ 
15:  end if
16: end while
17: return  $\hat{P} = \text{orth} \left( \begin{bmatrix} P_{s_{f_1}} & \dots & P_{s_{f_k}} \end{bmatrix} \right).$ 

```

Notice that $P_{s,i} \subseteq P_{s,i+1} \subseteq \mathbb{R}^n$ and hence there exists k_s such that $P_{s,k_s} = P_{s,k}$, $k \geq k_s$, and thus $P_{s,k} = R_s$,

$$R_s = \text{span}\{\text{im}(A_v G) \mid v \in Q^*, s_0 \rightarrow_v s\}.$$

Let $k = \max\{k_s \mid s \in S\}$. It then follows that $P_{s,k+1} = P_{s,k} = \text{im}(P_s)$ for all $s \in Q$ and hence after k iterations, the loop 4 will terminate. Moreover, in that case, $\text{im}(P_{s_{f_i}}) = R_{s_{f_i}}$, $i \in \{1, \dots, k\}$. But notice that for any $v \in Q^*$, $q \in Q$, $s_0 \rightarrow_v s_{f_i}$ if and only if $v \in K$, and $s_0 \rightarrow_{qv} s_{f_i}$ if and only if $qv \in K$, $i \in \{1, \dots, k\}$. Hence, $\sum_{s \in F} R_s = \mathcal{R}_K$ and thus $\text{im} \left(\begin{bmatrix} P_{s_{f_1}} & \dots & P_{s_{f_k}} \end{bmatrix} \right) = \mathcal{R}_K$. ■

Notice that the computational complexities of Algorithm 1 and Algorithm 2 are polynomial in n , even though the spaces of \mathcal{R}_L (resp. \mathcal{O}_L) might be generated by images (resp. kernels) of exponentially many matrices.

Using Algorithm 1 and 2, we can state Algorithm 3 for solving Problem 1. The matrices P and W computed in Algorithm 3 satisfy the conditions of Lemma 3 and Lemma 4 respectively. Lemma 3 – 4 then imply the following corollary.

Corollary 2 (Correctness of Algorithm 3): The LSS $\bar{\Sigma}$ returned by Algorithm 3 is a solution of Problem 1, i.e. $\bar{\Sigma}$ is L -equivalent to Σ and $\dim \bar{\Sigma} \leq \dim \Sigma$.

VII. NUMERICAL EXAMPLES

In this section, the model reduction method for DTLSSs with restricted discrete dynamics will be illustrated by 2 numerical examples. The data used for both examples and MATLAB codes for the algorithms stated in the paper are available online from <https://kom.aau.dk/~mertb/>. In the first example, it turns out that the rank of the P matrix from

Algorithm 2 Calculate a matrix representation of $\mathcal{O}_K(H)$,
Inputs: $(\{A_q\}_{q \in Q}, H)$ and $\mathcal{A} = (S, \{\rightarrow_q\}_{q \in Q}, F, s_0)$ such that $L(\mathcal{A}) = K$, $F = \{s_{f_1}, \dots, s_{f_k}\}$, $k \geq 1$ and \mathcal{A} is co-reachable.
Outputs: $\hat{W} \in \mathbb{R}^{\hat{r} \times n}$ such that $\hat{W} \hat{W}^T = I_{\hat{r}}$, $\text{rank}(\hat{W}) = \hat{r}$, $\ker(\hat{W}) = \mathcal{O}_K(H)$.

```

1:  $\forall s \in S \setminus F : W_s := 0.$ 
2:  $\forall s \in F : W_s^T := \text{orth}(H^T).$ 
3:  $\text{flag} = 0.$ 
4: while  $\text{flag} = 0$  do
5:    $\forall s \in S : W_s^{\text{old}} := W_s$ 
6:   for  $s \in S$  do
7:      $P_s := W_s$ 
8:     for  $q \in Q, s' \in S : s \rightarrow_q s'$  do
9:        $P_s := \begin{bmatrix} P_s \\ W_{s'}^{\text{old}} A_q \end{bmatrix}$ 
10:    end for
11:     $W_s^T := \text{orth}(P_s^T)$ 
12:  end for
13:  if  $\forall s \in S : \text{rank}(W_s) = \text{rank}(W_s^{\text{old}})$  then
14:     $\text{flag} = 1.$ 
15:  end if
16: end while
17: return  $\hat{W} = W_{s_0}.$ 

```

Lemma 3 is less than the rank of W from Lemma 4. In the second example, the opposite is the case. For both examples, we used the same NDFA from Figure 1 to define the set of admissible switching sequences: the NDFA is defined as the tuple $\mathcal{A} = (S, \{\rightarrow_q\}_{q \in Q}, F, s_0)$ where $S = \{s_0, s_1, s_f\}$, $\rightarrow_1 = \{(s_0, s_1)\}$, $\rightarrow_2 = \{(s_1, s_f)\}$, $\rightarrow_3 = \{(s_f, s_0)\}$ and $F = \{s_f\}$.

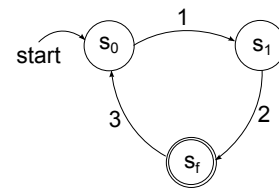


Fig. 1. The NDFA \mathcal{A} accepting the switching sequence language for both examples.

Observe that the language L accepted by the NDFA \mathcal{A} is the set $L = \{12, 12312, \dots\}$ and it can also be represented by the regular expression $L = (123)^* 12$. As stated in Definition 4, the labels of the edges represent the discrete mode indices of the DTLSS. The parameters of the single input - single output (SISO) DTLSS Σ of order $n = 7$ with $Q = \{1, 2, 3\}$ used for the first example are of the following form

Algorithm 3 Reduction for DTLSSs

Inputs: $\Sigma = (p, m, n, Q, \{(A_q, B_q, C_q) | q \in Q\}, x_0)$ and $\mathcal{A} = (S, \{\rightarrow_q\}_{q \in Q}, F, s_0)$ such that $L(\mathcal{A}) = L$, $F = \{s_{f_1}, \dots, s_{f_k}\}$, $k \geq 1$ and \mathcal{A} is co-reachable.

Output: $\bar{\Sigma} = (p, m, r, Q, \{(\bar{A}_q, \bar{B}_q, \bar{C}_q) | q \in Q\}, \bar{x}_0)$.

- 1: Compute a co-reachable NDFA $\hat{\mathcal{A}}_r$ from \mathcal{A} such that $L(\hat{\mathcal{A}}_r) = ((L)_1)_*$, where $((L)_1)_*$ is as in (12).
- 2: Use Algorithm 1 with inputs $(\{A_q\}_{q \in Q}, x_0)$ and NDFA $\hat{\mathcal{A}}_r$. Store the output \hat{P} as $P_{x_0} := \hat{P}$.
- 3: **for** $q \in Q$ **do**
- 4: Compute a co-reachable NDFA $\hat{\mathcal{A}}_{r,q}$ from \mathcal{A} such that $L(\hat{\mathcal{A}}_{r,q}) = {}^q(K)$, where ${}^q(K)$ is as in (16).
- 5: Use Algorithm 1 with inputs $(\{A_q\}_{q \in Q}, B_q)$ and NDFA $\hat{\mathcal{A}}_{r,q}$. Store the output \hat{P} as $P_q := \hat{P}$.
- 6: **end for**
- 7: $P = \text{orth}([P_{x_0} \ P_1 \ \dots \ P_D])$
- 8: **for** $q \in Q$ **do**
- 9: Compute a co-reachable NDFA $\hat{\mathcal{A}}_{o,q}$ from \mathcal{A} , such that $L(\hat{\mathcal{A}}_{o,q}) = (K)^q$, where $(K)^q$ is as in (18).
- 10: Use Algorithm 2 with inputs $(\{A_q\}_{q \in Q}, C_q)$ and NDFA $\hat{\mathcal{A}}_{o,q}$. Store the output \hat{W} as $W_q := \hat{W}$.
- 11: **end for**
- 12: $W^T = \text{orth}([W_1^T \ \dots \ W_D^T])$
- 13: **if** $\text{rank}(P) < \text{rank}(W)$ **then**
- 14: Let $r = \text{rank}(P)$, P^{-1} be a left inverse of P and set

$$\bar{A}_q = P^{-1}A_qP, \bar{C}_q = C_qP, \bar{B}_q = P^{-1}B_q, \bar{x}_0 = P^{-1}x_0.$$
- 15: **end if**
- 16: **if** $\text{rank}(P) \geq \text{rank}(W)$ **then**
- 17: Let $r = \text{rank}(W)$ and let W^{-1} be a right inverse of W . Set

$$\bar{A}_q = WA_qW^{-1}, \bar{C}_q = C_qW^{-1}, \bar{B}_q = WB_q, \bar{x}_0 = Wx_0.$$
- 18: **end if**
- 19: **return** $\bar{\Sigma} = (p, m, r, Q, \{(\bar{A}_q, \bar{B}_q, \bar{C}_q) | q \in Q\}, \bar{x}_0)$.

```

A1 = [zeros(7,1), randn(7,2), zeros(7,1), randn(7,3)]
A2 = [randn(7,1), zeros(7,1), randn(7,5)]
A3 = [randn(7,2), zeros(7,1), randn(7,4)]
B1 = [0, 1, 0, 0, 0, 0, 0]^T
B2 = [0, 0, 1, 0, 0, 0, 0]^T
B3 = [0, 0, 0, 1, 0, 0, 0]^T
C1 = randn(1,7)
C2 = randn(1,7)
C3 = randn(1,7)
x0 = [1, 0, 0, 0, 0, 0, 0]^T

```

where `randn` and `zeros` are the MATLAB functions which generates arrays containing random real numbers with standard normal distribution and zeros respectively. Applying Algorithm 3 to this DTLSS whose admissible switching sequences are generated by the NDFA shown in Figure 1, yields a reduced order system $\bar{\Sigma}$ of order $r = 4$, whose output values are the same as the original system Σ along

the allowed switching sequences. This corresponds to an L -realization in the sense of Definition 5 since the language L of the NDFA \mathcal{A} is defined as the set of all words generated by \mathcal{A} starting from its initial state and ending in a final state. In this example, it turns out the algorithm makes use of Lemma 3 and constructs the P matrix. The $P \in \mathbb{R}^{n \times r}$ matrix acquired is $P = \begin{bmatrix} I_4 \\ \mathbf{0} \end{bmatrix}$. Note that as stated in Remark 2, the resulting DTLSS is more than just an L -realization of Σ , its output coincides with the output of Σ for all instances along the allowed switching sequences, rather than just the instances corresponding to the final states of the NDFA (the switching sequence generated by \mathcal{A} used for simulating the examples is given in (19)). This fact is visible from Figure 2 where it can be seen that output of both systems corresponding to all instances along the switching sequence σ of length $|\sigma| = 11$ defined by

$$\sigma = 12312312312 \quad (19)$$

coincide (the input sequence of length 11 used in the simulation is also generated by the function `randn`). Finally, observe that the DTLSS $\bar{\Sigma}$ is minimal (note that the definition of minimality for linear switched systems are made by considering *all* possible switching sequences in Q^* [16]), however for the switching sequences restricted by the NDFA \mathcal{A} , it turns out 3 states are disposable. In fact, this is the main idea of the paper.

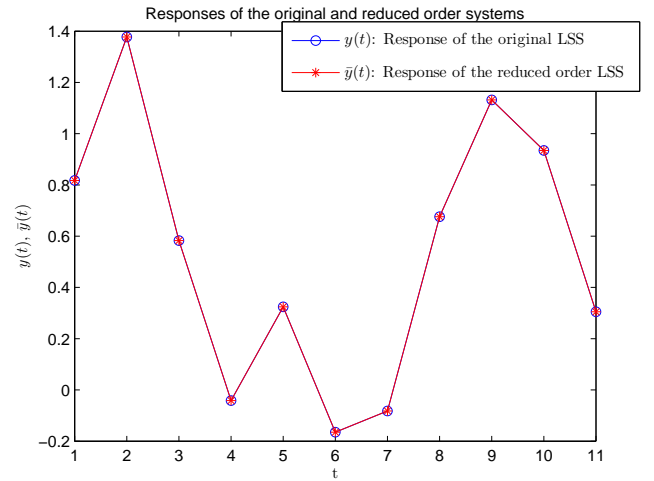


Fig. 2. Example 1: The responses of the original DTLSS Σ of order 7 and the reduced order DTLSS $\bar{\Sigma}$ of order 4 acquired by Algorithm 3 for the switching sequence in (19).

One more example will be presented to illustrate the case when a representation for the L -unobservability space is constructed. The NDFA \mathcal{A} accepting the language of allowed switching sequences is the same one used in the first example; whereas, this time the parameters of the SISO DTLSS Σ of order $n = 7$ with $Q = \{1, 2, 3\}$ are in the form:


```

A1 = [[0, 1, 0, 0, 0, 0, 0]; randn(6,7)]
A2 = [randn(2,7); [0, 0, 0, 0, 0, 0, 0]; randn(4,7)]
A3 = [randn(1,7); [0, 0, 1, 0, 0, 0, 0]; randn(5,7)]
B1 = randn(7,1)
B2 = randn(7,1)
B3 = randn(7,1)
C1 = randn(1,7)
C2 = [1, 0, 0, 0, 0, 0, 0]
C3 = randn(1,7)

```

Applying Algorithm 3 to this DTLSS, yields a reduced order system $\tilde{\Sigma}$ of order $r = 3$, whose output values are the same as the original system Σ for the instances when the NDFA reaches a final state. Note that this corresponds precisely to an L -realization in the sense of Definition 5 (the last outputs of Σ and $\tilde{\Sigma}$ are the same for all the switching sequences generated by the governing NDFA, i.e., for all $\sigma \in L(\mathcal{A})$). In this example, the algorithm makes use of Lemma 4 and constructs the W matrix. The matrix $W \in \mathbb{R}^{r \times n}$ computed is $W = \begin{bmatrix} I_3 & \mathbf{0} \end{bmatrix}$.

In this example, note that the resulting DTLSS is merely an L -realization of Σ and nothing more as stated in Remark 3, i.e., its output coincides with the output of Σ for only the instances corresponding to the final states of the NDFA. This fact is visible from Figure 3, where it can be seen that output corresponding to the final state s_f of the NDFA coincides for Σ and $\tilde{\Sigma}$ (Observe that for all switching sequences generated by \mathcal{A} ending with the label 2, the output values of Σ and $\tilde{\Sigma}$ are the same). Again, the input sequence of length 11 used in the simulation is generated by the function `randn`. Finally, note that the DTLSS Σ is again minimal whereas for the switching sequences restricted by the NDFA \mathcal{A} , it turns out 4 states are disposable in this case.

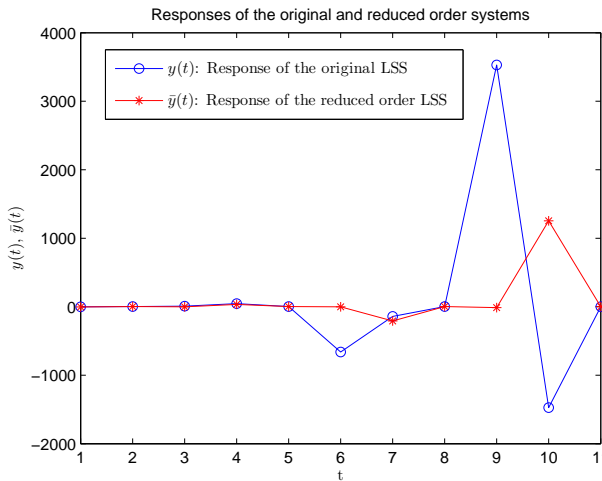


Fig. 3. Example 2: The responses of the original DTLSS Σ of order 7 and the reduced order DTLSS $\tilde{\Sigma}$ of order 3 acquired by Algorithm 3 for the switching sequence in (19). Note that only the second and fifth element of the output sequence is equal for Σ and $\tilde{\Sigma}$ in first five elements, others look the same as a result of the scaling.

VIII. CONCLUSIONS

A model reduction method for discrete time linear switched systems whose discrete dynamics are restricted by switching sequences comprising a regular language is presented. The method is essentially a moment matching type of model reduction method, which focuses on matching the Markov parameters of a DTLSS related to the specific switching sequences generated by a nondeterministic finite state automaton. Possible future research directions include expanding the method for continuous time case, and approximating the input/output behavior of the original system rather than exactly matching it, and formulating the presented algorithms in terms of bisimulation instead of input-output equivalence.

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